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Continuing Education Course #154
Free Open-Source Software (FOSS)
in Engineering

1. Free, Open Source Software (FOSS) availability is a relatively new phenomenon:
 a. True,
 b. False.
2. The term “free” in Free, Open Source Software most closely refers to:
 a. price,
 b. operating system dependence,
 c. free speech,
 d. free beer.
3. The advocates in favor of software patents:
 a. True,
 b. False.
4. A Linux operating system distribution is available as Free, Open Source Software (FOSS) from:
 a. Red Hat,
 b. GNU Project,
 c. Xubuntu,
 d. Both Red Hat & Xubuntu.
5. VirtualBox software can support multiple “guest” operating system concurrently:
 a. True,
 b. False.
6. VirtualBox software can run under one of several different “host” operating systems:
 a. True,
 b. False.
7. The OpenOffice Software Suite provides free of charge:
 a. a word processing program,
 b. a spreadsheet program,
 c. a presentation software program,
 d. all of the above.
8. The Eclipse Project was originally created by IBM :
 a. True,
 b. False.
9. The GNU Octave language is mostly compatible with the proprietary Matlab software:

- a. True,
 b. False.
10. The University of California at Berkeley Ptolemy II Project is currently written in the Java language:
 a. True,
 b. False.
11. A wide range of Free, Open Source Software (FOSS) is available from the sourceforge website:
 a. True,
 b. False.
12. The Silicon Renaissance Initiative is aimed at preventing beach erosion:
 a. True,
 b. False.
13. Major barriers-to-entry for Small and Medium Enterprise (SME) Integrated Circuit (IC) design efforts include:
 a. the cost of EDA software tools,
 b. the cost of fabrication access,
 c. the cost of verification and production testing,
 d. all of the above.
14. The gEDA Project offers Free, Open Source Software (FOSS) tools for:
 a. schematic capture,
 b. circuit simulation,
 c. printed circuit design,
 d. all of the above.
15. A tutorial for using gEDA tools is available online from Cambridge University:
 a. True,
 b. False.
16. Icarus Verilog can be used with the gtkwave tool for verilog design, simulation and graphical waveform display:
 a. True,
 b. False.
17. The Integrated Circuit (IC) specific Free, Open Source Software (FOSS) EDA tools supported by Dr Tim Edwards at opencircuitdesign.com are totally different from gEDA tools:
 a. True,
 b. False.
18. The FOSS Magic tool has been used in university and SME environments since the mid-1980s:
 a. True,
 b. False.
19. The Magic tool suite automatically generates schematics:
 a. True,
 b. False.
20. The IRSIM tool is used to simulate digital circuit behavior:
 a. True,
 b. False.
21. The netgen tool is used to compare netlists:

- a. True,
- b. False.

22. Dr Tim Edwards at opencircuitdesign.com is developing a set of tools for digital design:

- a. True,
- b. False.

23. Complete designs are available as FOSS from OpenCores.org:

- a. True,
- b. False.

24. The VHDL language is used in the Alliance digital design flow:

- a. True,
- b. False.

25. Lax foreign enforcement of copyrights may give foreign firms a competitive advantage:

- a. True,
- b. False.

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